

15 RISC and CISC – Part 1 — Answers

Answers

Work through these after trying the questions yourself.

15.1 CISC.

15.2 RISC.

15.3 ARM or RISC-V (any one).

15.4 Load and store.

15.5 Because registers are very fast (memory is slow).

15.6 RISC instructions are simple and regular, so the chip can be smaller, use less power and give off less heat — all valuable in a device running on a battery.

15.7

```
LOAD  R1, a
LOAD  R2, b
ADD   R3, R1, R2
STORE R3, c
```